

S/N 09/820898

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Girish P. Ramanathan et al.

Examiner: Unknown

Serial No.: 09/820898

Group Art Unit: Unknown

Filed: March 30, 2001

Docket: 884.963US1

Title: SYSTEM FOR VARYING TIMING BETWEEN SOURCE AND DATA
SIGNALS IN A SOURCE SYNCHRONOUS INTERFACE

Assignee: Intel Corporation

Customer No: 21186

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CERTIFICATE UNDER 37 CFR § 3.73(b)

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Technology Center 2100

In accordance with 37 C.F.R. Section 1.36, M.P.E.P. Section 402.05 and 402.07, please
revoke any existing Powers of Attorney, if any, and appoint the following attorneys and/or patent
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STATEMENT UNDER 37 CFR § 3.73(b)

Intel Corporation is the assignee of the entire right, title and interest in the patent application identified above by virtue of an assignment from the inventor filed July 10, 2001 and recorded on Reel 011969, Frames 0891 - 0893.

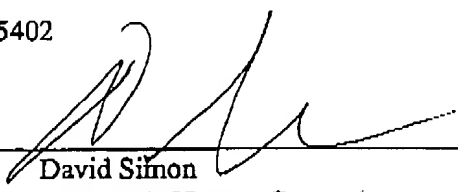
Pursuant to 37 C.F.R. §3.73(b) I hereby declare that I, David Simon, am empowered to sign this statement on behalf of Intel Corporation, the assignee.

Please direct all correspondence in this case to:

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Date January 18, 2004

By


David Simon

Title: Chief Patent Counsel